

0306-341 HW #6 Spring 2009

Due Wed 4-29-00

Determine and use the best way to describe the functionality of the following sequential logic elements. You should include the symbol, circuit diagram, truth table, some timing diagram, and 'your own words.' The objective of this homework is to let 'you' summarize the sequential logic elements to the way you would understand and remember. It is however not necessary to be long, but rather organized and easy to read. Note that some elements (e.g., S-R latch) may have more than one circuit for implementation (different truth tables). You may choose the way you want to understand the differences.

- a. S-R latch
- b. D-latch
- c. D-Flipflop
- d. J-K Flipflop
- e. T Flipflop