

Exercise 9: Design and Simulation of a Serial Adder

Lab Component		Point Value	Points Earned	Comments
Signatures	<i>Prelab</i>	20		1 2
	<i>Demo</i>	40		1 2
Abstract		4		
Design Methodology	Serial Adder	<i>Discussion of Design</i>	4	
		<i>Circuit Diagram</i>	4	
	Serial Adder/ Subtractor	<i>Discussion of Design</i>	4	
		<i>Circuit Diagram</i>	4	
Results and Analysis	Serial Adder	<i>Simulation Results</i>	4	
		<i>Discussion of Results</i>	4	
	Serial Adder/ Subtractor	<i>Simulation Results</i>	4	
		<i>Discussion of Results</i>	4	
Conclusion		2		
TOTAL		100		