Exercise 8: Design and Simulation of a Three-Bit Ring Counter and a Four-Bit Shift Register

Lab Component			Point Value	Points Earned	Comments
Signatures	***Prelab***		20		1 2
	Demo		40		1 2
Abstract			3		
Design Methodology	**3-bit ring counter**	***Discussion of Circuit Functionality***	3		
		Circuit Diagram	3		
	Shift-Register	***Discussion of Circuit Functionality***	3		
		Circuit Diagram	3		
Results and Analysis	**3-bit ring counter**	***Simulation Results***	3		
		Discussion	3		
	Shift-register	***Simulation Results***	3		
		Discussion	3		
Conclusion			3		
Questions	***Q1***		2		
	Q2		2		
	Q3		2		
	Q4		2		
	Q5		2		
TOTAL			100		