

Exercise 7: Sequential Circuit Elements

Lab Component		Point Value	Points Earned	Comments
Signatures	<i>Prelab</i>	20		1 2
	<i>Demo</i>	40		1 2
Abstract		4		
Design Methodology	<i>D Latch with Enable</i>	<i>Discussion</i>	4	
		<i>Circuit Diagram</i>	4	
	<i>D Flip-Flop</i>	<i>Discussion</i>	4	
		<i>Circuit Diagram</i>	4	
Results and Analysis	<i>D Latch with Enable</i>	4		
	<i>D Flip-Flop</i>	4		
Conclusion		4		
Questions	<i>Q1</i>	4		
	<i>Q2</i>	4		
TOTAL		100		