

Exercise 6: Combinational Logic Circuit Design Using Decoders and Multiplexers

Lab Component		Point Value	Points Earned	Comments
Signatures	<i>Prelab</i>	20		
	<i>Demo</i>	40		
Abstract		4		
Design Methodology	<i>Parking Lot Control Circuit Discussion</i>	6		
	<i>Circuit Diagram</i>	10		
Results and Analysis	<i>Parking Lot Control Circuit</i>	6		
Conclusion		4		
Question		10		
TOTAL		100		