

Lab Exercise 5: Binary Addition and Subtraction Circuits

Lab Component		Point Value	Points Earned	Comments
Signatures	<i>Prelab</i>	20		Part 1 Part 2
	<i>Demo</i>	40		Part 1 Part 2
Abstract		3		
Design Methodology	<i>1-Bit FA</i>	<i>Discussion and K-map</i>	3	
		<i>Circuit Diagram</i>	3	
	<i>4-Bit Adder/Sub-tracter</i>	<i>Discussion of add/subtract control</i>	4	
		<i>Circuit Diagram</i>	3	
Results and Analysis	<i>1-Bit Full Adder</i>	3		
	<i>4-Bit Adder/Subtractor</i>	4		
Conclusion		3		
Questions	<i>Q 1</i>	4		
	<i>Q 2</i>	2		
	<i>Q 3</i>	3		
	<i>Q 4</i>	2		
	<i>Q 5</i>	3		
TOTAL		100		