

### Exercise 3: Combinational Logic Circuit Design Using Boolean Algebra Simplification

Lab Component		Point Value	Points Earned	Comments
Signatures	<i>Prelab</i>	20		Part 1 Part 2 Part 3
	<i>Demo</i>	40		
Abstract		3		
Design Methodology	<i>Truth Table</i>	4		
	<i>Boolean Expressions</i>	4		
	<i>Boolean Algebra Simplification</i>	8		
	<i>Schematic</i>	4		
	<i>Discussion</i>	4		
Results and Analysis	<i>Discussion</i>	4		
Conclusion		3		
Questions	<i>EGC</i>	2		
	<i>Chip Count</i>	2		
	<i>Input Wire Count</i>	2		
TOTAL		100		